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UNITED STATES PATENT AND TRADEMARK OFFICE

JAN 7 2002

Application of:

Roman J. Hamerski and Walter R. Buchanan

Serial No. 09/923,647

Filed: August 7, 2001

APPARATUS AND METHOD FOR
FABRICATING A HIGH REVERSE
VOLTAGE SEMICONDUCTOR DEVICE

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Docket No. 12263.15
MAIL ROOM

Confirmation No. 1145

Group Art Unit: 2822

Customer No.: 27526

TC 2800 MAIL ROOM

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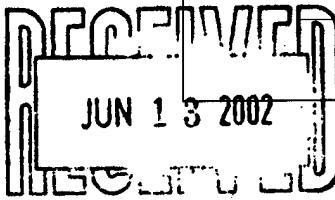
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on October 26, 2001.

Kyle L. Elliott



INFORMATION DISCLOSURE STATEMENT

Applicant submits herewith the patents of which it is aware, which it believes may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made (37 CFR 1.56(g)), an admission that the information cited is, or is considered to be, material to patentability or that no other material information exists.

The filing of this Information Disclosure Statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

Any such material patents, publications or other information, which is known to Applicant, is listed on a copy of PTO Form 1449 (modified) and legible copies of such listed

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information are enclosed. After reasonable inquiry, and based on the information in the Applicant's attorney's files, no other information material to examination of the above-referenced application is known to Applicant.

Respectfully submitted,



10-26-01

Date

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